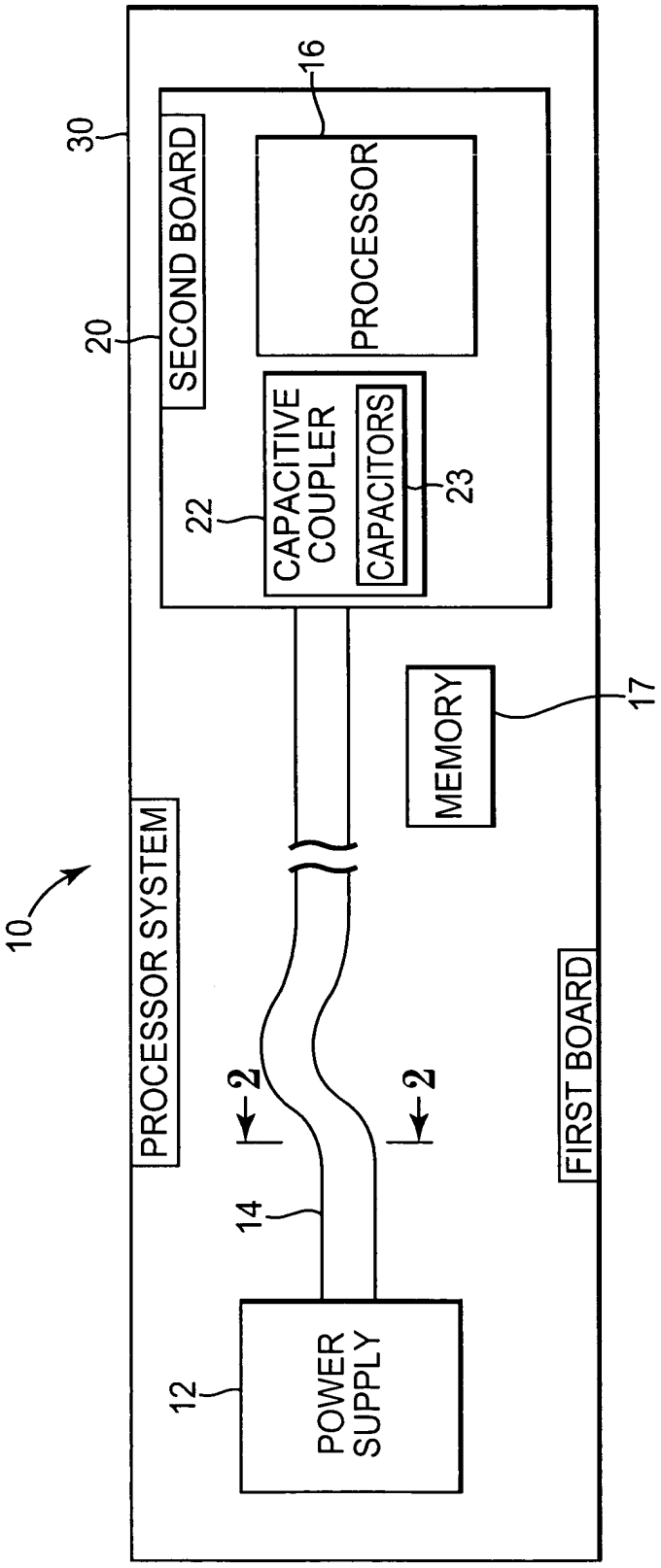

10
PROCESSOR SYSTEM
FIRST BOARD
SECOND BOARD
30
20
16
PROCESSOR
CAPACITIVE COUPLER
CAPACITORS
22
23
MEMORY
17
POWER SUPPLY
12
14
2
2

Fig. 1

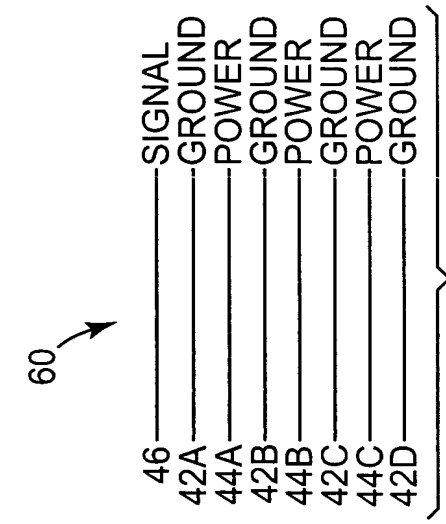


Fig. 2C

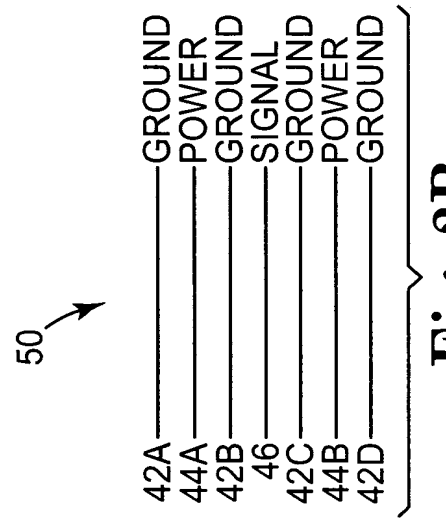


Fig. 2B

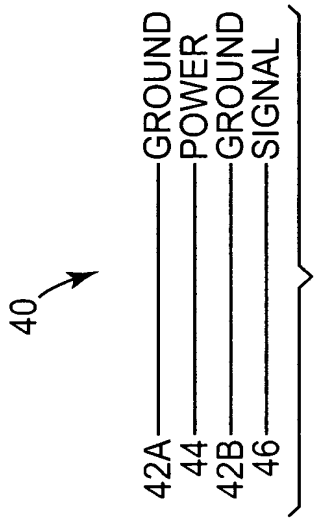


Fig. 2A

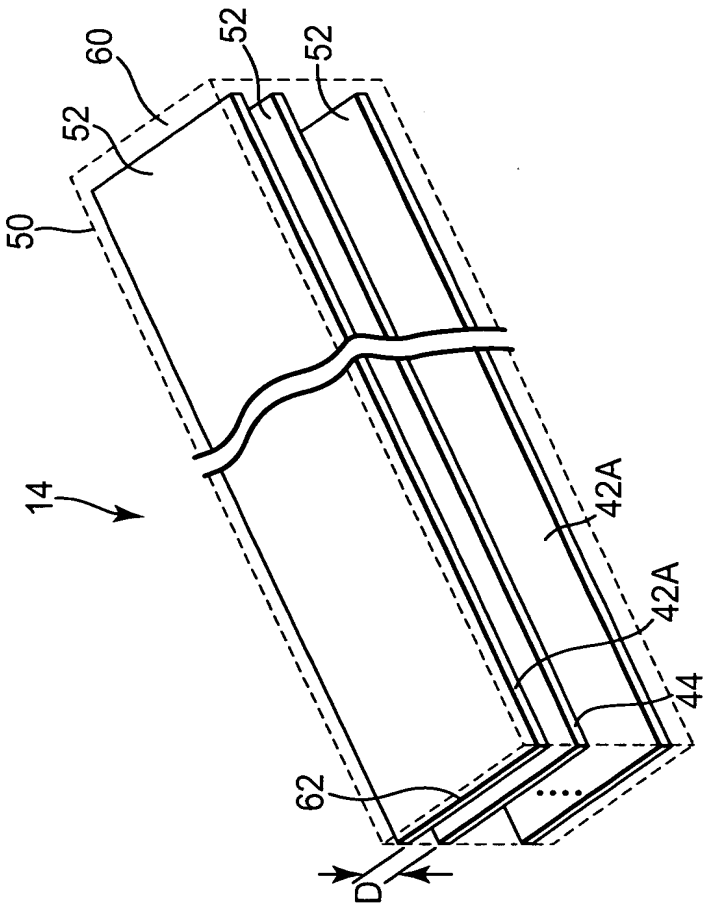


Fig. 3

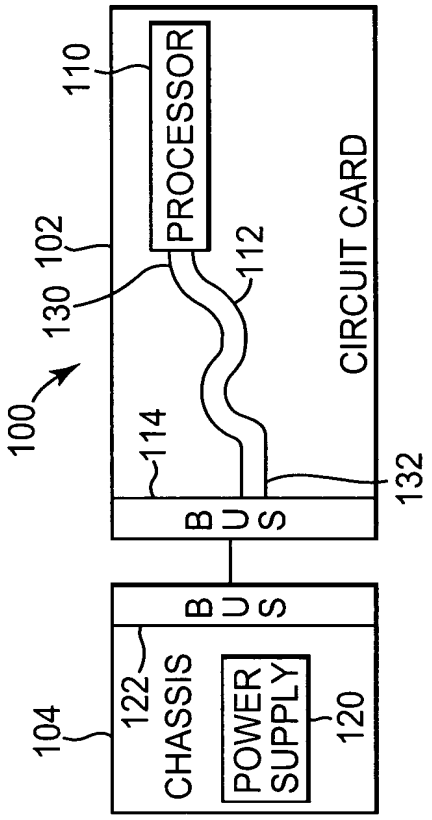
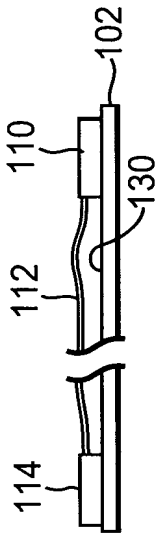

100
102
104
110
112
114
120
122
130
132
PROCESSOR
CIRCUIT CARD
BUS
BUS
CHASSIS
POWER SUPPLY

Fig. 4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110
112
114
130

Fig. 5